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Facsimile Transmittal

DATE: November 22, 2004

TO: USPTO

ATTN: BOX: INFORMATION DISCLOSURE STATEMENT

EXAMINER: Edward Urban

RE: Serial No. 10/092,669

FAX: (703) 872-9306

FROM: George C. Pappas

Number of Pages Sent: (including this transmittal cover sheet)

ATTACHED HERETO IS AN INFORMATION DISCLOSURE STATEMETN IN (2) PAGES; PTO-1449; FOUR (4) REFERENCES. PLEASE CALL ME IF YOU HAVE ANY QUESTIONS.

I hereby certify that this correspondence is being sent VIA PACSIMILE to the Commissioner of Patents at fax number (703) 8729306. Attention Office of Amendments, on:

11/22/04

(Date of Deposit)

Darla D. Kasmerto

(Name of the Person Making the Deposit)

(Signature)

PAGE 232: RCVD AT 14/22/2004 7:27:04 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-1/2 * DNIS:8729306 * CSID: + * DURATION (mm-ss):11-24

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of)	
) For:	CALIBRATION TECHNIQUES
Jeremy D. Dunworth, et al.)	FOR FREQUENCY
)	SYNTHESIZERS
	}	RECEIVED
Serial No. 10/092,669	ý	CENTRAL FAX CENTER
)	NOV 2 2 2004
Filed: March 6, 2002) Group No.	

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Applicants submit herewith, in accordance with 37 CFR § 1.98, the patents, publications and other information listed on the enclosed Form PTO-1449.

CERTIFICATE OF MAJLING/TRANSMISSION (37 CFR 1.8(a))

I hereby certify that this correspondence is, on the date shown below, being:

MAILING

deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date: 11/22/04

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transmitted by facsimile to the Patent and Trademark Office.

Depositor's Name Darla Kasmedo

Signature:

PAGE 3/32 * RCVD AT 11/22/2004 7:27:04 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-1/2 * DNIS:8729306 * CSID:+* DURATION (mm-ss):11-24

The references submitted herewith are those of which Applicants are aware, which they believe may be material to the examination of this application and, with respect to which there may be a duty to disclose in accordance with 37 CFR § 1.56. While the references identified may be material to the examination of this application pursuant to 37 CFR § 1.56, the citation of these references is not intended to constitute an admission that any reference referred to herein is prior art to the invention of this application, unless specifically designated as such. Additionally, the filing of this Information Disclosure Statement shall not be construed to mean that any search has been made or, that if made, such search was complete or exhaustive, or that no other material information as defined in 37 CFR § 1.56 exists.

In accordance with 37 CFR § 1.97(b)(3), this Information Disclosure Statement is being submitted before mailing of the first Office Action on the merits. Each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application.

A list of the references is set forth on Form PTO-1449, which is enclosed herewith, along with a copy of each cited foreign reference (copies of U.S. references are not enclosed). Applicants respectfully request that the Examiner return to Applicants a copy of the Form PTO-1449 indicating consideration of the references cited thereon.

Applicants do not believe that any fees are due. If, however, it is determined that fees are owed, Applicants hereby authorize that such fees be charged to Deposit Account No. 17-0026.

Respectfully submitted,

Dated: 11/22/04

George C. Pappas, Reg. No. 35,065

858-651-1306

QUALCOMM Incorporated Attn: Patent Department 5775 Morehouse Drive San Diego, California 92121-1714

Telephone:

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(858) 658-2502

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Sheet 1 of 3 ATTY. DOCKET NO. 010481 SERIAL NO. 10/092,669 FORM PTO-1445 U.S. DEPARTMENT OF COMMERCE (REV. 7-80) PATIENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT APPLICANT Dunworth, et al GROUP 2682 DATE MAILED: 11/22/04 March 6, 2002 U.S. PATENT DOCUMENTS PRIATE EXAMINER No DOCUMENT NUMBER DATE NAME CLASS CLASS 10 3/2/74 Allen, et al. 9 1 3 2 σ. 1/26/82 Orugket, et al. Λ3 4 5 • 4 8 3 6/12/84 Baylor **A**4 7 9 2 7 ø 8 12/20/88 Priod, ct al. ۸s 4 9 . 0 4 3 2/20/90 Niwa 0 9 5 Helfrick 4 9 4 0 7/10/90 A6 Caldwoll, et al. 0 2 3 5 2 6/11/91 Δ7 AB 2 1/7/92 Owon at at. <u> 88</u> 3 3 3 5 8/2/94 Ballantyne, et al ΔIQ 9 1/17/95 Garsbeah, at Al <u> 12</u> 5 4 Z., 2 4 12/5/95 Jentsi, et el. A13 4 9 3 7 • 2/20/96 Takahara et al. A14 8 2 8 2 o 10/27/98 Cougt 8 ٩ A12 . 1 4 8 5 11/3/98 Netson, et al. 7 8 0 Ostorling, ot al. AIG 2/9/99 7/20/99 1/18/00 Q:5hayshnony
FOREIGN PATENT DOCUMENTS 1/18/00 EXAMINER ROY INITIAL No DOCUMENT NUMBER COUNTRY NAME DATE 5 2 6 8 0 7 8 10/15/93 JP Kobayashi Hideaki 2 0 0 1 2 3 0 0 4 2 3 9 4 1 BZ 6 6 8/24/01 æ Marconi Instruments Limited 9/19/90 EPO **B**3 OTHER PRIOR ART (Including Author, Title, Date, Pertinent Page, Bic.)

"A Low-Noise Multiple Output Phase Locked Loop Synthesizer for Radar Applications" European Frequency and Time Forum (2002) CI "Design of an Integrated CMOS PLL Frequency Synthesizer" IEEE C2 EXAMINER